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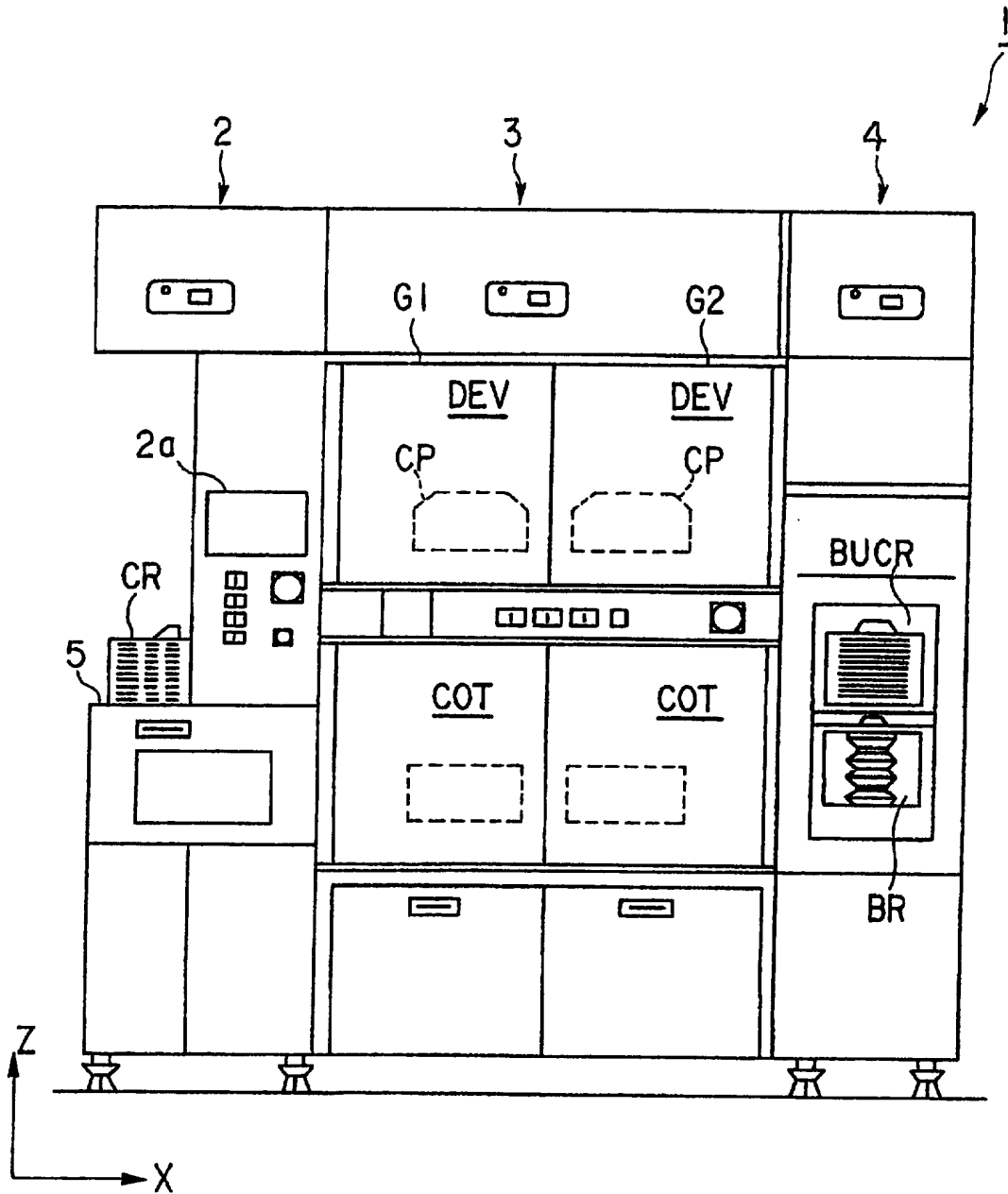


FIG. 2

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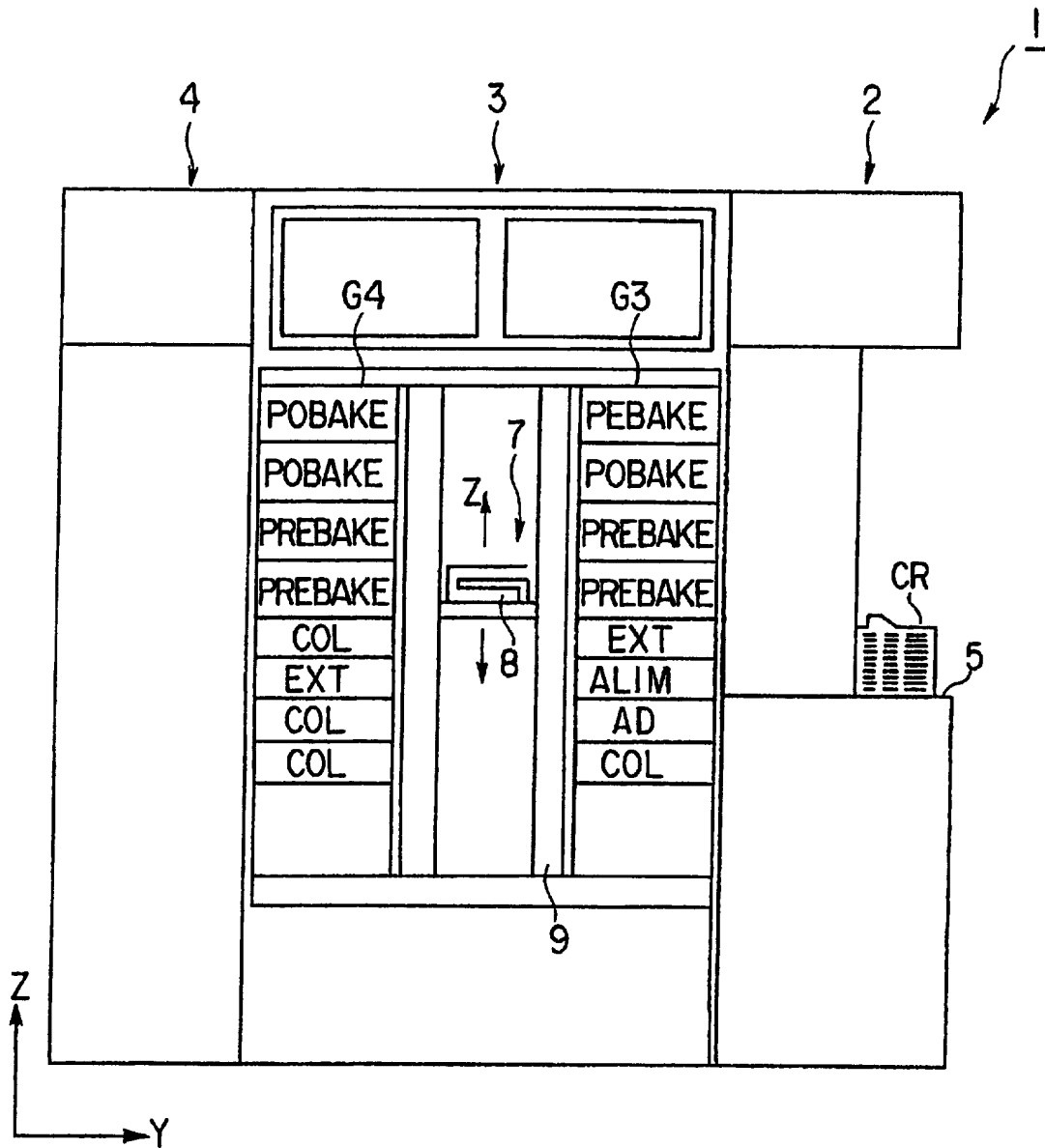


FIG. 3

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G1	G2	G3	G4
DEV1	DEV2	POBAKE1	PEBAKE
COT1	COT2	POBAKE2	POBAKE3
		PREBAKE1	PREBAKE3
		PREBAKE2	PREBAKE4
EXP		COL1	EXT
		EXT	ALIM
		COL1	AD
		COL2	COL4
		COL3	

FIG. 4

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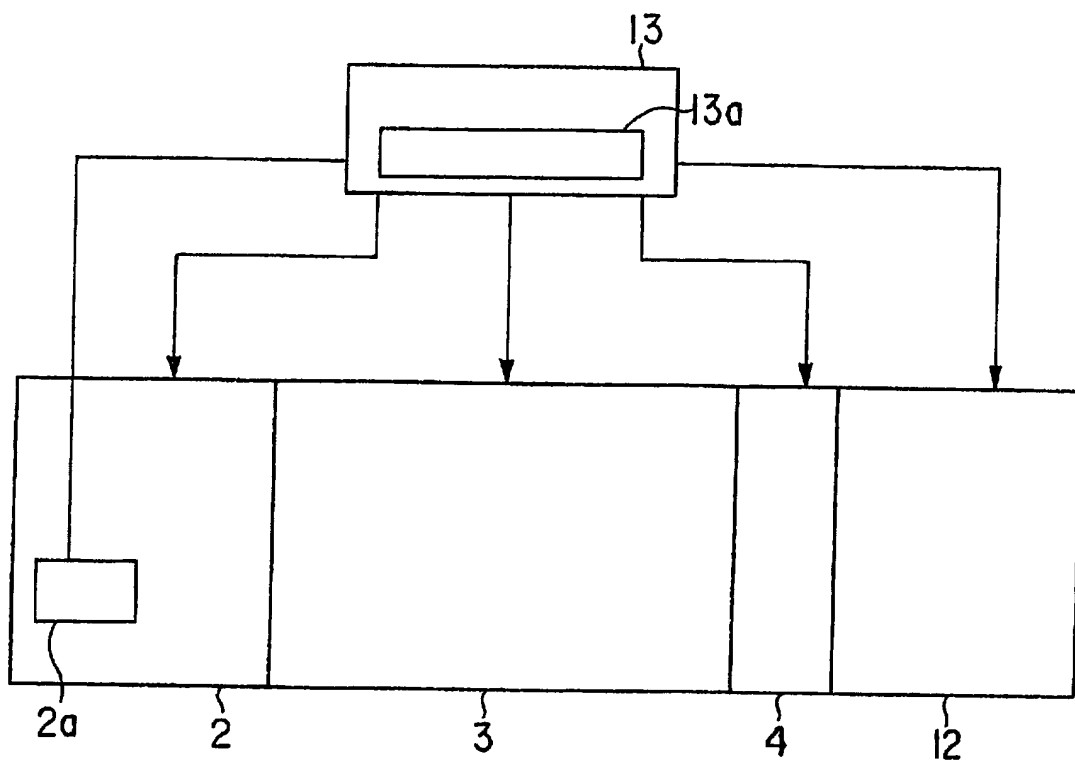


FIG. 5

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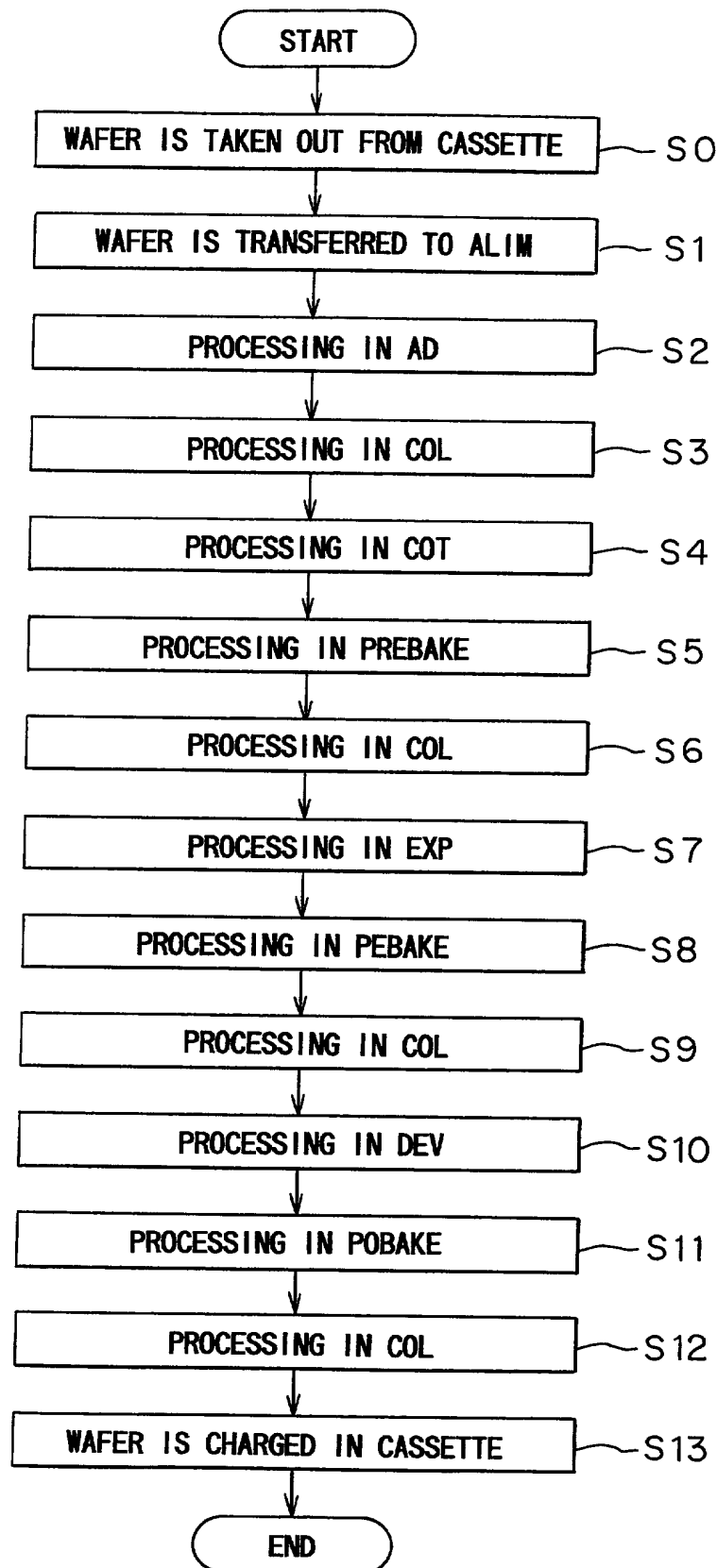


FIG. 6

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STEP	PROCESSING UNITS
S1	ALIM
S2	AD
S3	COL
S4	COT
S5	PREBAKE
S6	COL
S7	EXP
S8	PEBAKE
S9	COL
S10	DEV
S11	POBAKE
S12	COL

FIG. 7





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(ALIM)	(AD)	(COL)	(COT)	(PRE)	(COL)	(EXP)	(PEB)	(COL)	(DEV)	(POB)	(COL)
t1	t2	t3	t4	t5	t6	t7	t8	t9	t10	t11	t12

FIG. 9

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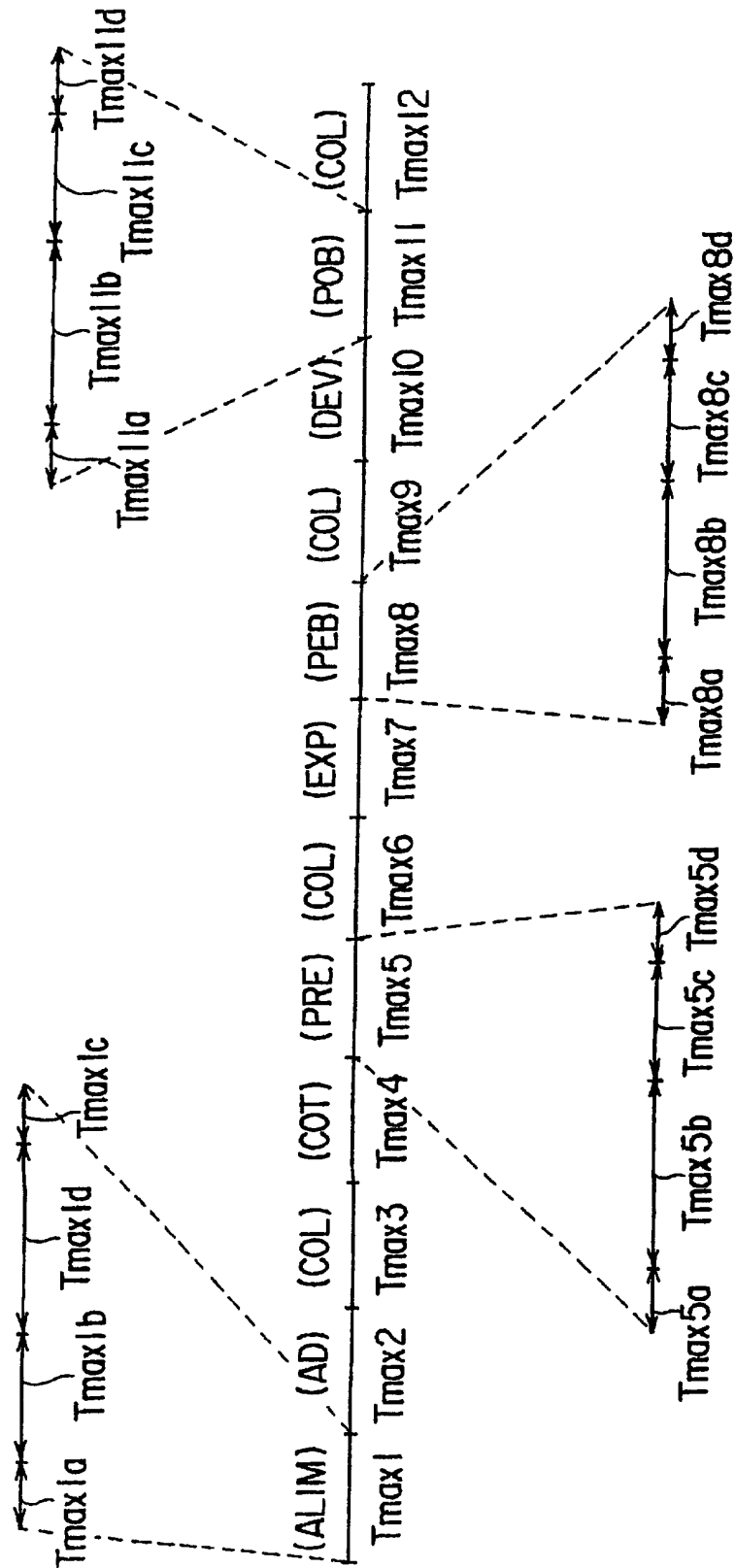


FIG. 10

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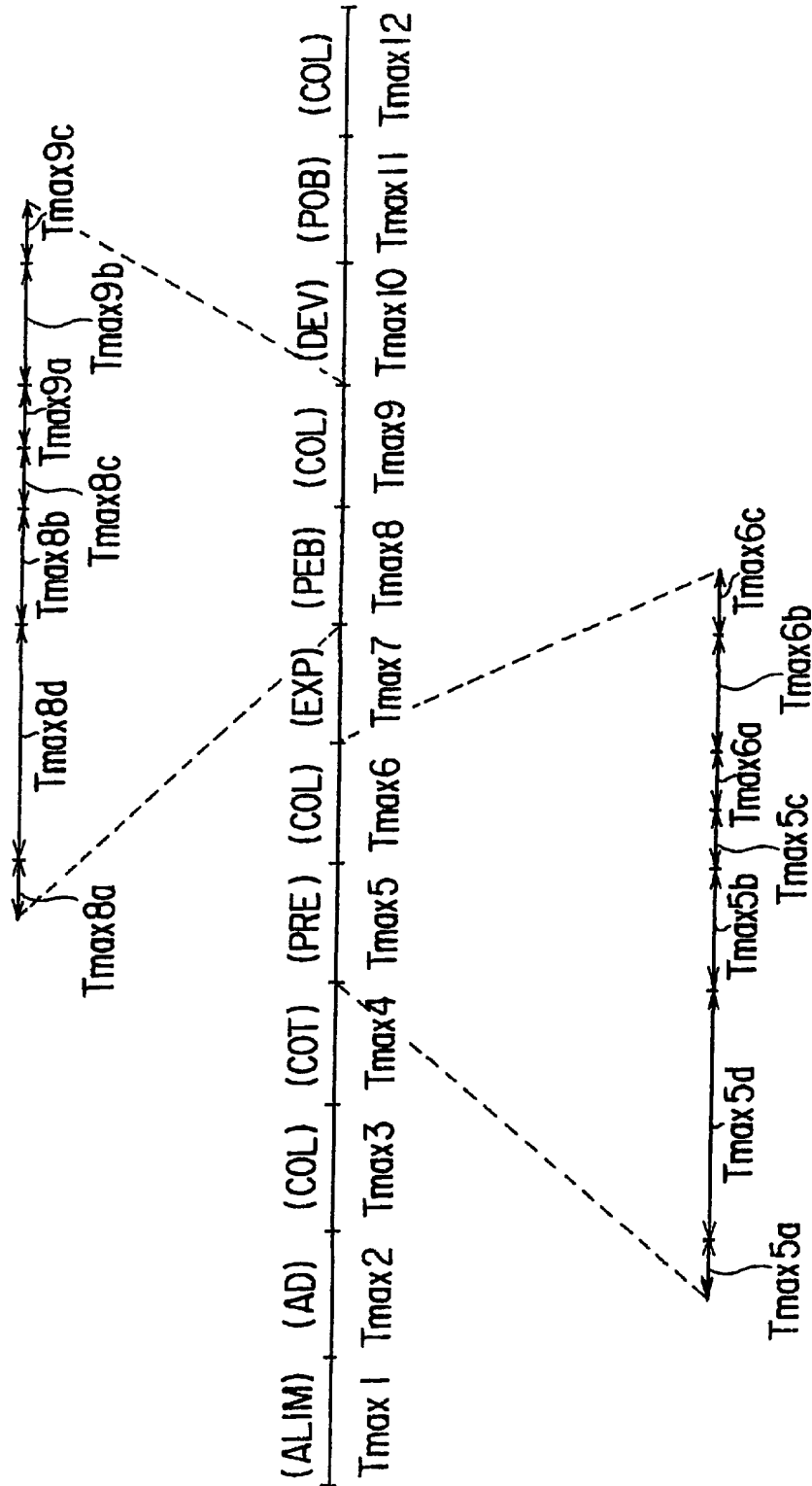


FIG. 11

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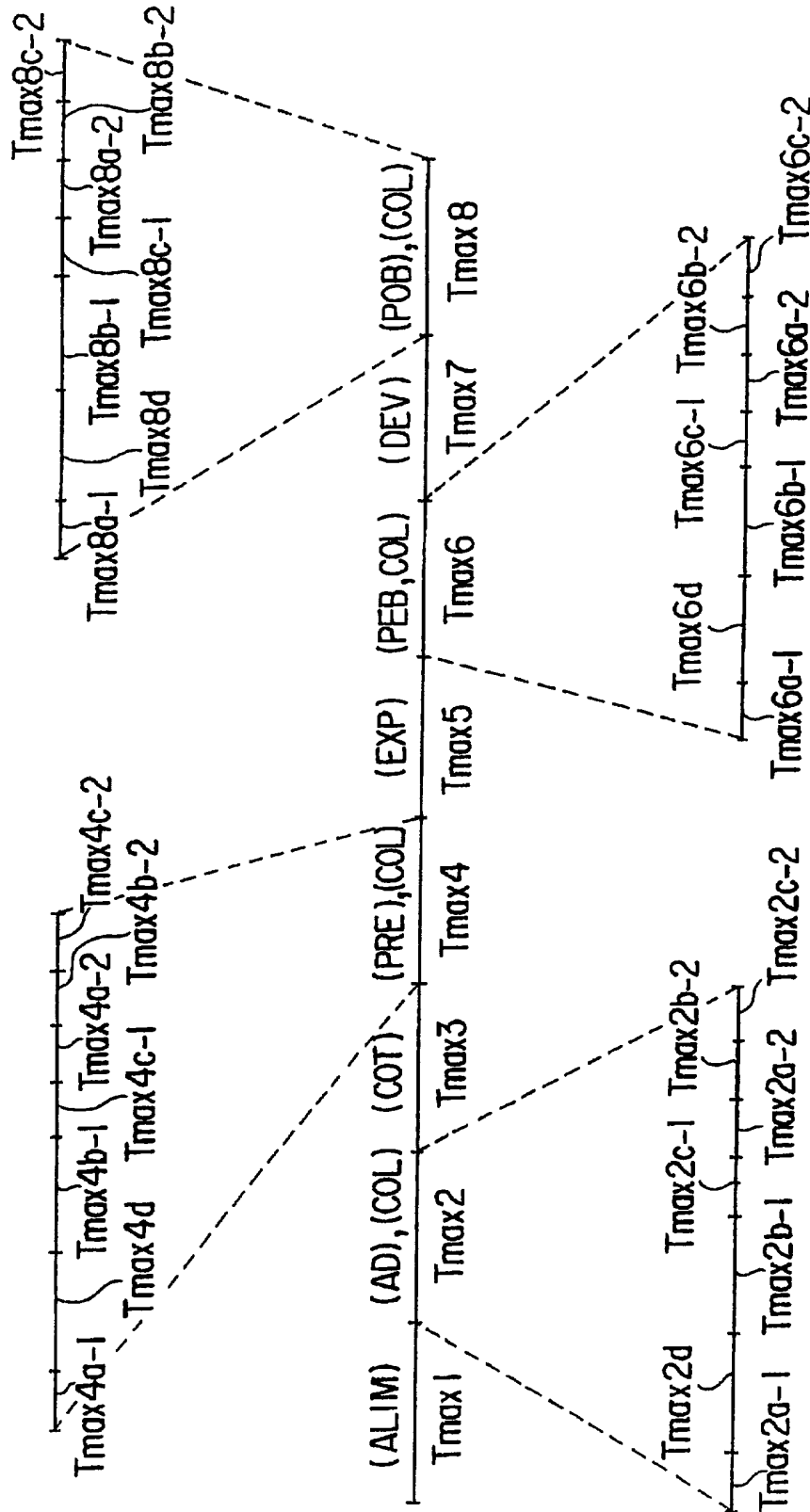


FIG. 12

FIG. 13